IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	ISAO YAMAMOTO ET AL.	)
		)
For:	SEMICONDUCTOR DEVICE HAVING VOLTAGE FEEDBACK CIRCUIT THEREIN, AND ELECTRONIC APPARATUS USING THE SAME	)))

CLAIM FOR PRIORITY

Mail Stop Patent Application
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Applicants hereby claim the benefits of the filing date of October 4, 2002 to Japanese Application No. 2002-292513 and September 12, 2003 to Japanese Application No. 2003-322295 under provisions of 35 U.S.C. 119 and the International Convention for the protection of Industrial Property.

If any fees are due with regard to this claim for priority, please charge them to Deposit Account No. 06-1130 maintained by Applicants' attorneys.

Respectfully submitted,

CANTOR COLBURN LLP

By _____
Lisa A. Bongiovi
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I certify that this correspondence is being deposited with the United States Postal Service as Express mail in an envelope addressed to: Mail Stop Patent Application, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on

October 2, 2003
(Date of Deposit)

Patricia A. Hart
(Name of Person Mailing Paper)

_____ 10/02/03
Signature Date

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